

Fig. 1 is a cross-sectional view of a device. It shows a substrate 3 with a layer 42 on top. A vertical dimension t_1 is indicated for the layer 42.

This diagram shows a second embodiment of the device. It features a base layer 31 and a top layer 42. The top layer 42 is composed of multiple rectangular blocks arranged in a staggered pattern, with some blocks overlapping others. The base layer 31 is a solid, uniform layer beneath the top layer.

Fig.2a

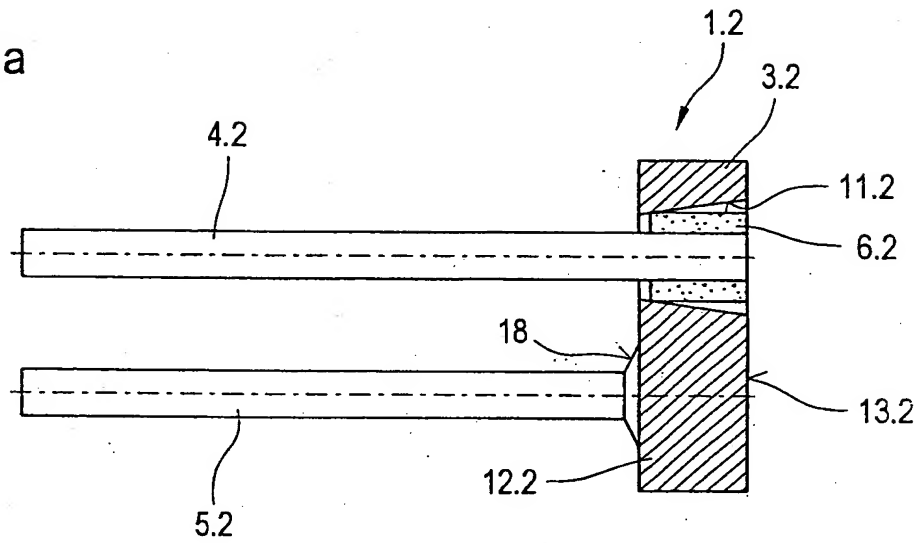


Fig.2b

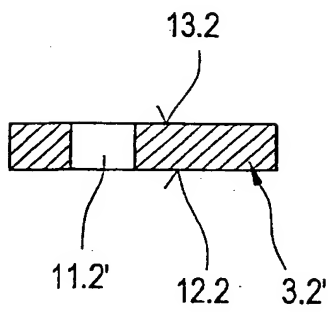


Fig.2c

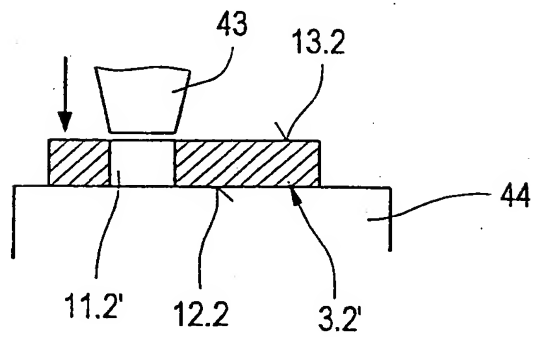


Fig.3

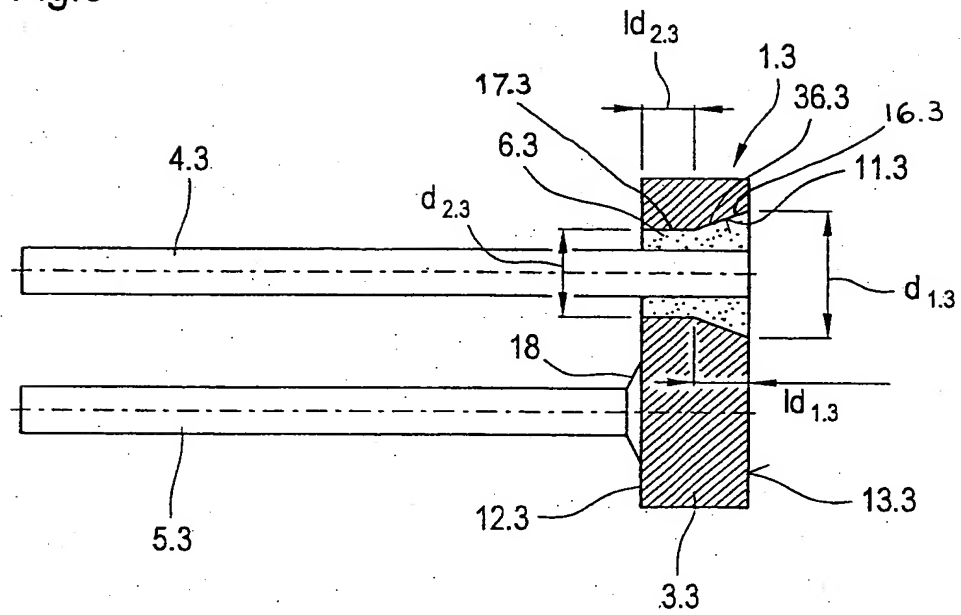


Fig.4

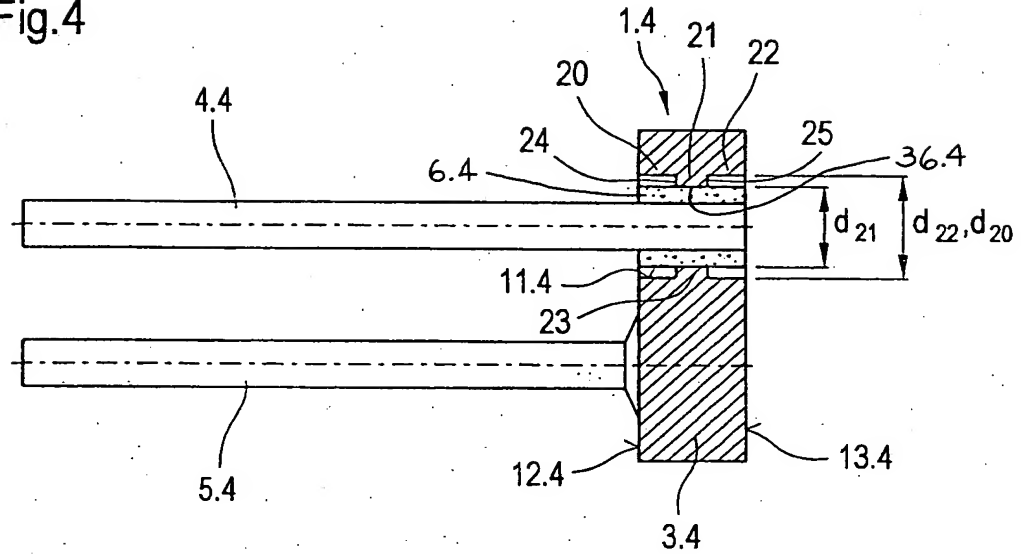


Fig.5

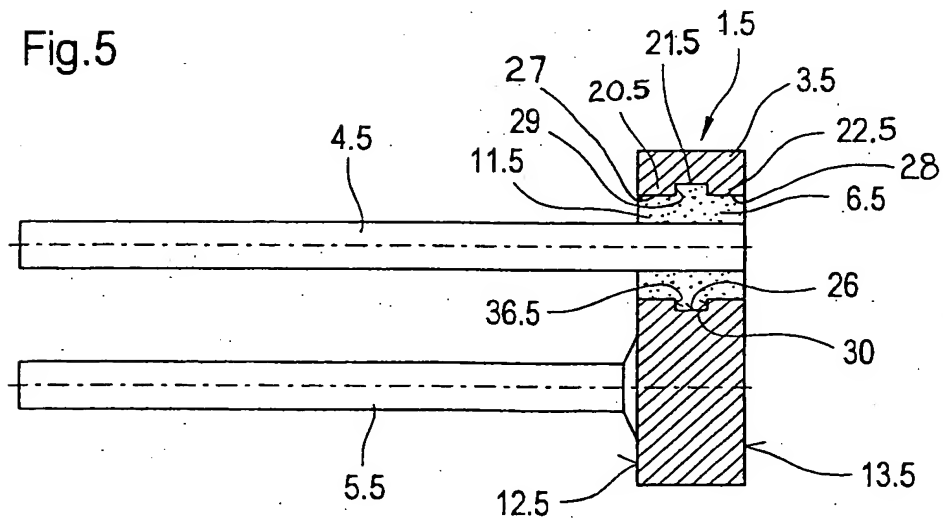


Fig.6

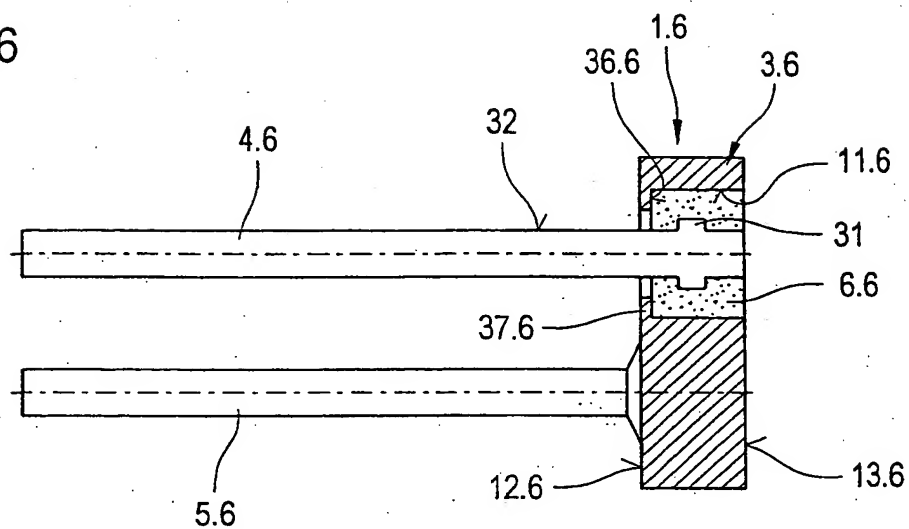


Fig.7

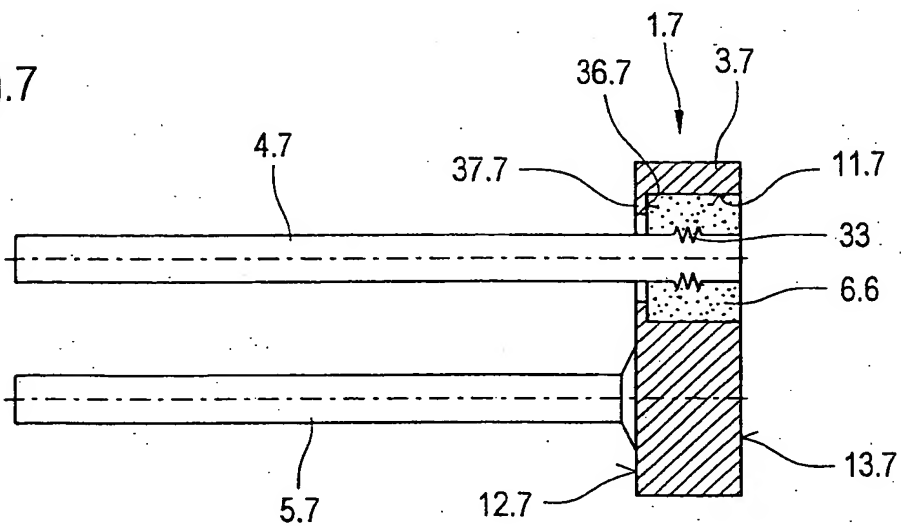


Fig.8

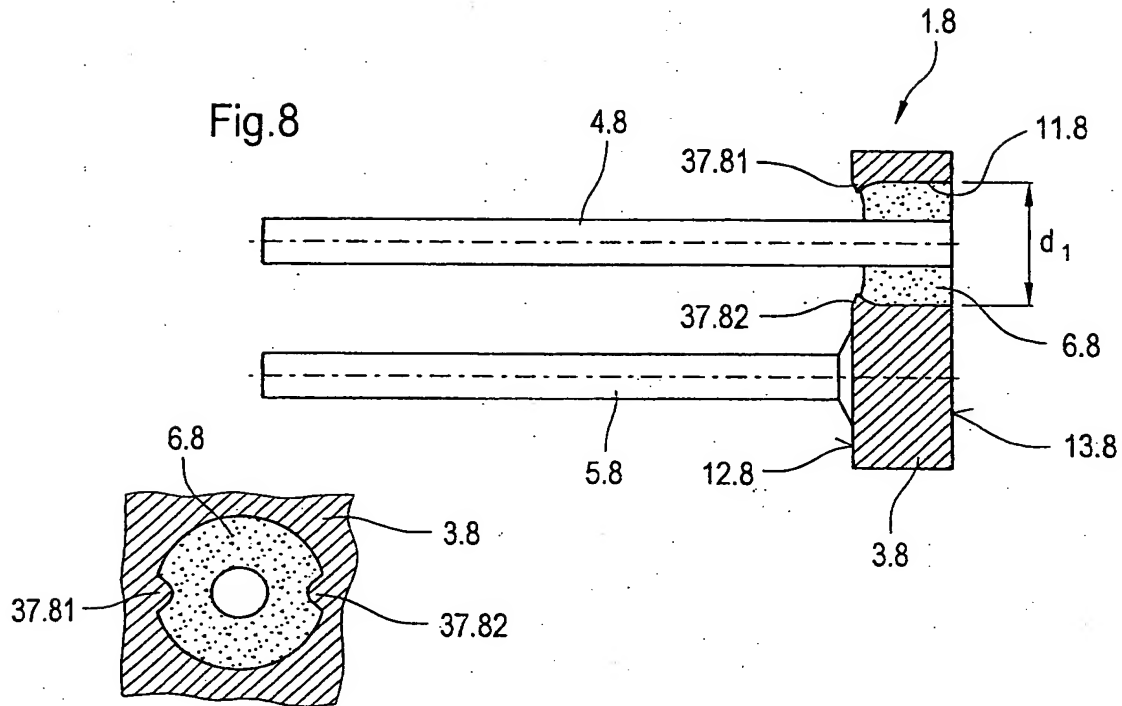


Fig.9

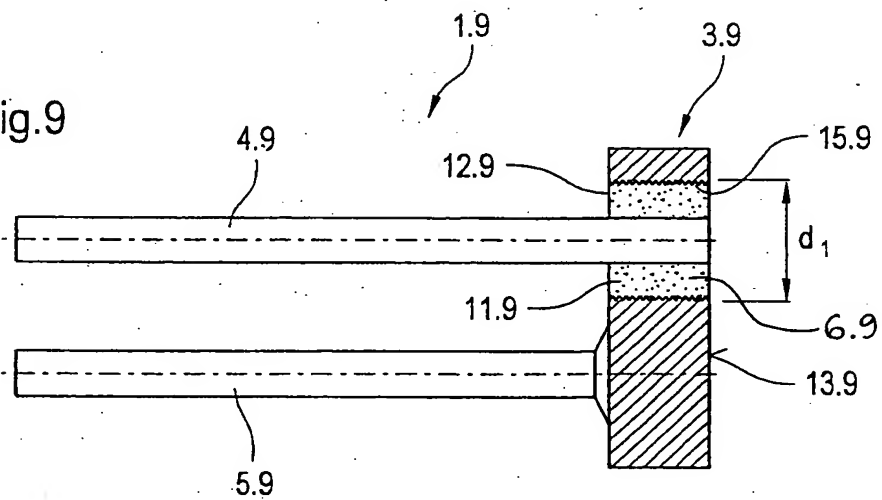


Fig.10

